TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title	of
Invent	ion

METHOD FOR FABRICATING A SEMICONDUCTOR TRANSISTOR DEVICE HAVING ULTRA-SHALLOW SOURCE/DRAIN EXTENSIONS

Application Number:

Date:

First Named Applicant:

Yun-Ren Wang

Confirmation Number:

Attorney Docket Number:

NAUP0591USA

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Documents being submitted:	Files
us-assignment	NAUP0591_0_A1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	NAUP0591_0_A1ASS1.tif
	NAUP0591_0_A1ASS2.tif
us-request	NAUP0591_0_A1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	NAUP0591_0_A1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-declaration	NAUP0591_0_A1-usdecl.xml
	us-declaration.dtd
	us-declaration.xsl
us-power-of-attorney-grant	NAUP0591_0_A1-uspoat.xml
	us-power-of-attorney-grant.dtd
	us-power-of-attorney-grant.xsl
us-declaration	NAUP0591_0_A1DEC1.tif
us-declaration	NAUP0591_0_A1DEC2.tif
application-body	NAUP0591-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	Naup0591usa-01.tif
	Naup0591usa-02.tif

	Naup0591usa-03.tif	
	Naup0591usa-04.tif	
	Naup0591usa-05.tif	
Comments		